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Shayla E. Dunn
Shayla E. Dunn
Date of Signature 3/20/02



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Ronalf Kramer

Group Art Unit: Not Assigned

Serial No.: 10/052,652

Examiner: Not Assigned

Filed: January 18, 2002

Docket No.: 1406/36

Confirmation No.: 5317

For: CIRCUIT FOR GENERATING AN ASYNCHRONOUS SIGNAL PULSE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.56, 1.97, and 1.98, applicants' undersigned attorney brings to the attention of the Patent and Trademark Office the following references. Form PTO/SB/08A is attached hereto. Copies of the references are also enclosed. This is not to be construed as a representation that a search has been made or that a reference is relevant merely because cited.

EP Patent No. 0 072 686 to Suzuki et al. discloses a buffer circuit including inverter circuitry.

DE Patent No. 199 25 374 to Tommaso Bacigalupo discloses a schaltungen und verfahren zum einstellen eines digitalen potentials.

JP Patent No. 59004327 to Terasawa Tomizou discloses an output buffer circuit.

Early passage of the subject application to issue is earnestly solicited.

Appln. No.: 10/052,652

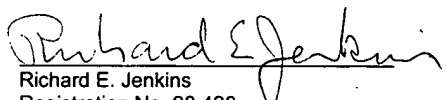
Although a check is being submitted, the Commissioner is hereby authorized to charge any deficiencies of payment associated with the filing of this Information Disclosure Statement to Deposit Account No. 50-0426.

Respectfully submitted,

JENKINS & WILSON, P.A.

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Enclosures

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Sheet 1 of 1

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.